

## SForum 2006

Monday (August 28th)

#	Time	
*	08:00	REGISTRATION STARTS

### Tuesday: Tutorial I

#	Time	Title and Tutorialist
*	14:00	<b>ADVANCED ULTRA LOW-POWER BIOMEDICAL SIGNAL PROCESSING BASED ON ANALOG WAVELET TRANSFORM</b>  S. Haddad - Delft University of Technology, The Netherlands

### Tuesday: Session I (oral presentations)

#	Time	Title and Authors
1	15:50	<b>KARMA: A DIDACTIC TOOL FOR KARNAUGH MAPS</b>  CARLOS E. KLOCK, FELIPE R. SCHNEIDER, MATEUS V. N. GOMES, RENATO P. RIBAS and ANDRE I. REIS
2	16:10	<b>EFFICIENT SIMULATION OF SINGLE-ELECTRON DYNAMICS IN COUPLED QUANTUM DOTS</b>  Andre Calmon, Marcus Batistuta and José Camargo
3	16:30	<b>NUMERICAL ANALYSIS OF AN InSb INFRARED RADIATION PHOTODETECTOR</b>  Felipe Lorenzo Della Lucia and Jacobus Swart
4	16:50	<b>LOW POWER DEDICATED DATAPATHS FOR LMS ADAPTIVE FILTER</b>  DOUGLAS FOSTER, NATHALIA ARDIZZONE, MARCELO FONSECA, SERGIO MELO and EDUARDO COSTA
5	17:10	<b>BLUEGUM: USE OF BLUETOOTH INTERFACES IN THE ACCESS OF PORTABLE EQUIPMENT</b>  Karen Oliveira, Emilena Specht and Luigi Carro
6	17:30	<b>INITIAL HIGH LEVEL CACTI ESTIMATES AND PHYSICAL DESIGN IMPLEMENTATION OF FAST SRAM CACHES</b>  Eduardo Conrad Junior, Eric Ericson Fabris and Sergio Bampi

POSTER SESSION: SForum

Tue. Aug. 29th, 17:50-19:30, Wed. Aug. 30<sup>th</sup>, 10:10-10:40 and 15:20-15:50 (during coffee breaks)

#	Title and Authors  Poster presenters are strongly encouraged to stand by their posters at all times indicated
<i>PSF1</i>	<b>AUTOMATIC ADL-BASED GENERATION OF DISASSEMBLING TOOLS</b>  Alexandre K. I. Mendonça, Felipe G. Carvalho, Max R. de O. Schultz, Luiz C. V. Santos and Olinto J. V. Furtado
<i>PSF2</i>	<b>TRANSISTOR PLACEMENT FOR NONDUAL CMOS CELL SYNTHESIS</b>  Mário C. B. Osório, Renato P. Ribas and André I. Reis
<i>PSF3</i>	<b>CELL LIBRARY VERIFICATION METHODOLOGY</b>  Simone Bavaresco, Caio G. P. Alegretti, Renato P. Ribas and André I. Reis
<i>PSF4</i>	<b>A VLSI PROTOTYPE OF A 32-BIT RISC PROCESSOR</b>  Flávio Calmon
<i>PSF5</i>	<b>ENTROPY CODER ARCHITECTURES FOR H.264/AVC BASELINE PROFILE</b>  Thaísa Silva, João Vortmann, José Güntzel, Sergio Bampi and Luciano Agostini
<i>PSF6</i>	<b>AN IMPROVED PARAMETER EXTRACTION METHOD USING A RF SOFTWARE SIMULATION</b>  Fellipe Grillo Peternella, Leandro Tiago Manera and José Alexandre Diniz
<i>PSF7</i>	<b>PALM- BASED INTERFACE FOR PORTABLE POTENTIOSTAT</b>  Luiz Menezes, Juliana Cardoso and Marcelo Bariatto
<i>PSF8</i>	<b>IMAGE MOSAIC CONSTRUCTION ON DSP</b>  Yumi Monma, Thiago Figueiró, André Soares, Letícia Guimarães and Altamiro Susin
<i>PSF9</i>	<b>DESIGN OF AN ALL-DIGITAL PHASE LOCKED LOOP: ANALYSIS AND CHARACTERIZATION IN FPGAS</b>  Luciano de Paula, Altamiro Susin and Sergio Bampi
<i>PSF10</i>	<b>Automatic Analysis of SET Propagation in CMOS Combinational Circuits</b>  Carolina Neves, José Luís Güntzel and Fernanda Kastensmidt
<i>PSF11</i>	<b>LEAKAGE CURRENT IN SUBMICRON PARTIALLY DEPLETED SOI NMOSFETS OPERATING AT HIGH TEMPERATURES</b>  Luciano Mendes Almeida and Marcello Bellodi
<i>PSF12</i>	<b>DEVELOPMENT OF A LASER ULTRASOUND SYSTEM FOR ELASTIC CHARACTERIZATION OF MICROSTRUCTURES</b>  Thiago Rueda, Cassius Ramos, Ednei do Amaral, Henrique Menegaz and Demartonne Ramos França

<i>PSF13</i>	<b>OPTOELECTRONIC PROPERTIES OF CERAMIC SEMICONDUCTOR FILMS</b> Frederico Mendes, Felipe Cândido, Déborah Alvarenga and Wagner Rodrigues
<i>PSF14</i>	<b>The Successive Refinement Methodology in the Embedded Systems Design</b> Sandro Renato Dias and Diógenes Cecílio Silva Júnior
<i>PSF15</i>	<b>CATHETER BASED BIOSENSOR:ASSEMBLING AND CHARACTERIZATION</b> Fernando Luis de Almeida and Marcelo Bariatto Andrade Fontes
<i>PSF16</i>	<b>HEART BEAT DETECTION USING WAVELET TRANSFORM</b> Marcelo S. Stoco, Rodrigo V. Andreão and Marcelo V. Segatto
<i>PSF17</i>	<b>Integrated Digital Systems Design Methodology</b> Ramon Risério and Diógenes Cécilio Silva Jr
<i>PSF18</i>	<b>TEST STRUCTURES TO EVALUATE A CELL LIBRARY</b> Carlos Afonso Ferreira Silva, Giovanni Sartori, André Inácio Reis and Renato Perez Ribas
<i>PSF19</i>	<b>A VLSI IMPLEMENTATION OF A LINEAR CELLULAR AUTOMATA TEST-BED</b> Flávio Calmon and Felipe Miranda
<i>PSF20</i>	<b>WIRELESS ECG MONITORING WITH PARTIAL TIME TRANSCEIVER USE</b> Emilio Bumachar, Rodrigo Andreão and Marcelo Segatto
<i>PSF21</i>	<b>COARSE-GRAINED RECONFIGURABLE ARCHITECTURES</b> Alisson Garcia and Ricardo Ferreira
<i>PSF22</i>	<b>CHARGE DISTRIBUTION IN TRIPLE-GATE DEVICES AT THRESHOLD VOLTAGE</b> Rodrigo Bechelli and Renato Giacomini
<i>PSF23</i>	<b>BDDeiro: BDD Visualization Tool</b> Mateus V. N. Gomes, Carlos E. Klock, Tiago M. G. Cardoso, Renato P. Ribas and André I. Reis
<i>PSF24</i>	<b>Fabrication and Electrical Characterization of MOS Capacitors - Chemical Cleaning Evaluation</b> Rodrigo Marques and Victor Sonnenberg
<i>PSF25</i>	<b>PDesigner Framework</b> Millena Gomes, Williams Azevedo, André Souza and Renato Oliveira